

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Howard Levy et al.

Title: PULSED DYNAMIC KEEPER GATING

Application No.: 10/607,949

Filed: June 27, 2003

Examiner: Tan, Vibol

Group Art Unit: 2819

Atty. Docket No.: 004-8850

February 11, 2004

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- ☒ Form(s) PTO-1449
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Nicole Teitler Cave 2/11/04
Nicole Teitler Cave Date

Respectfully submitted,

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U.S. Department of Commerce, Patent and Trademark Office		Attorney Docket No.: 004-8850
		Application No.: 10/607,949
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		Applicant(s): Howard Levy et al.
(Use several sheets if necessary)		Filing Date: June 27, 2003
		Group Art Unit: 2819
		Date Submitted: February 11, 2004
NON PATENT LITERATURE DOCUMENTS		
Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
	AA	NEIL H. E. WESTE and KAMRAN ESHRAGHIAN, Principles of CMOS VLSI Design, A Systems Perspective, Second Edition, Addison-Wesley Publishing Co., Copyright 1993 by AT&T, pp. 298-303.
	AB	
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